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 09/784151
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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED	Q.A.

APPLICATION NO. 09/784151	CONT/PRIOR F	CLASS 380 713	SUBCLASS 168	ART UNIT 207 2132 2181	EXAMINER Dovs G. Haynes
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Shigeru Hashimoto

TITLE IC, IC-mounted electronic device, debugging method and IC debugger

FTO-2040
12/99

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.			ISSUE BATCH NUMBER	
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